EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3811	(716/5,8-11).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/07/17 12:06
L2	37	(sub near3 circuit same schematic and extract$3 and flat)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 12:07
L3	4	(sub near3 circuit) same (schematic and extract$3 and flat)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 12:08
L4	0	((sub near3 circuit) same (schematic and extract$3 and flat)).clm.	USPAT	OR	ON	2006/07/17 12:09
L5	0	((layout model parameter channel length finger width number left middle right FET)).clm.	USPAT	AND	ON	2006/07/17 12:10
L6	180	((layout model parameter channel length finger width number left middle right FET))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/17 12:10
L7	3	((layout model parameter channel length finger width number left middle right FET)) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/17 12:11
L8	0	((layout model parameter channel length finger width number left middle right FET)).clm.	USPAT	AND	ON	2006/07/17 12:11